

6

5

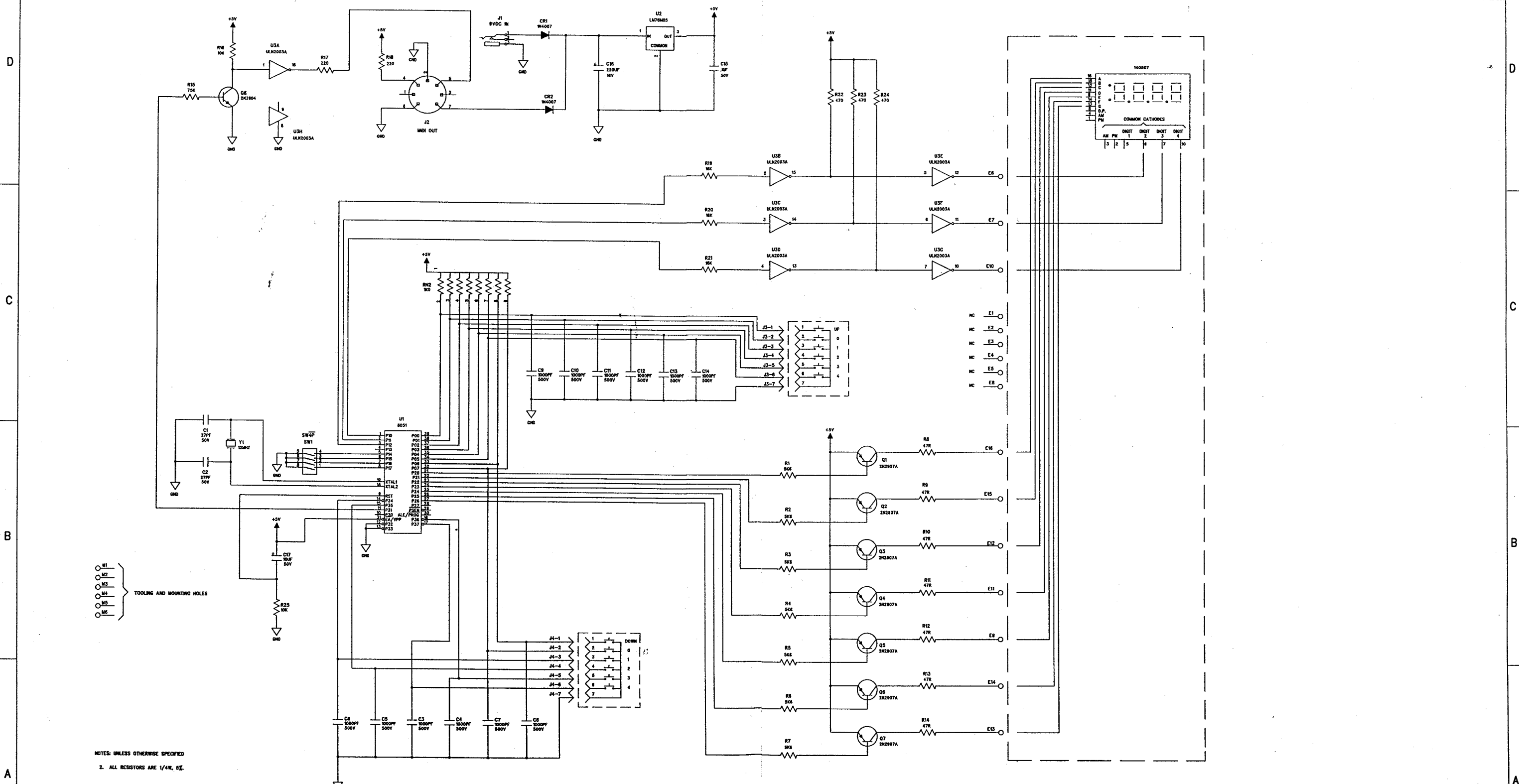
4

3

2

1

REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE
A	RELEASED		11-3-83



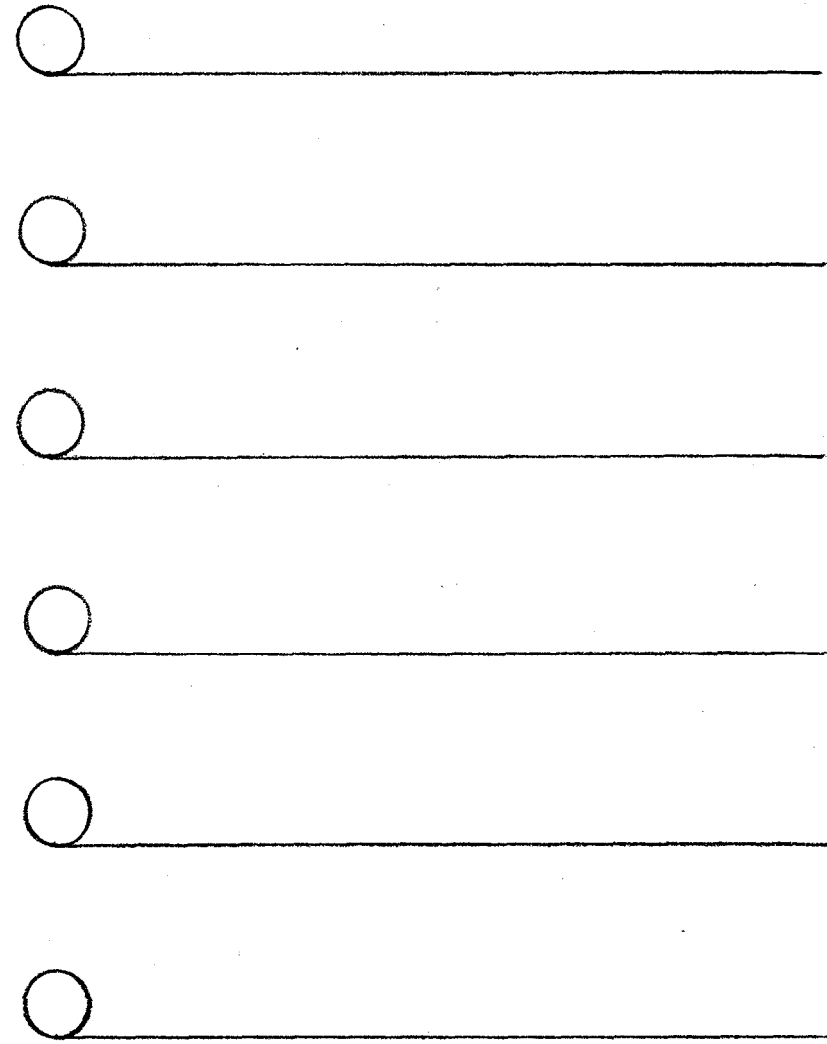
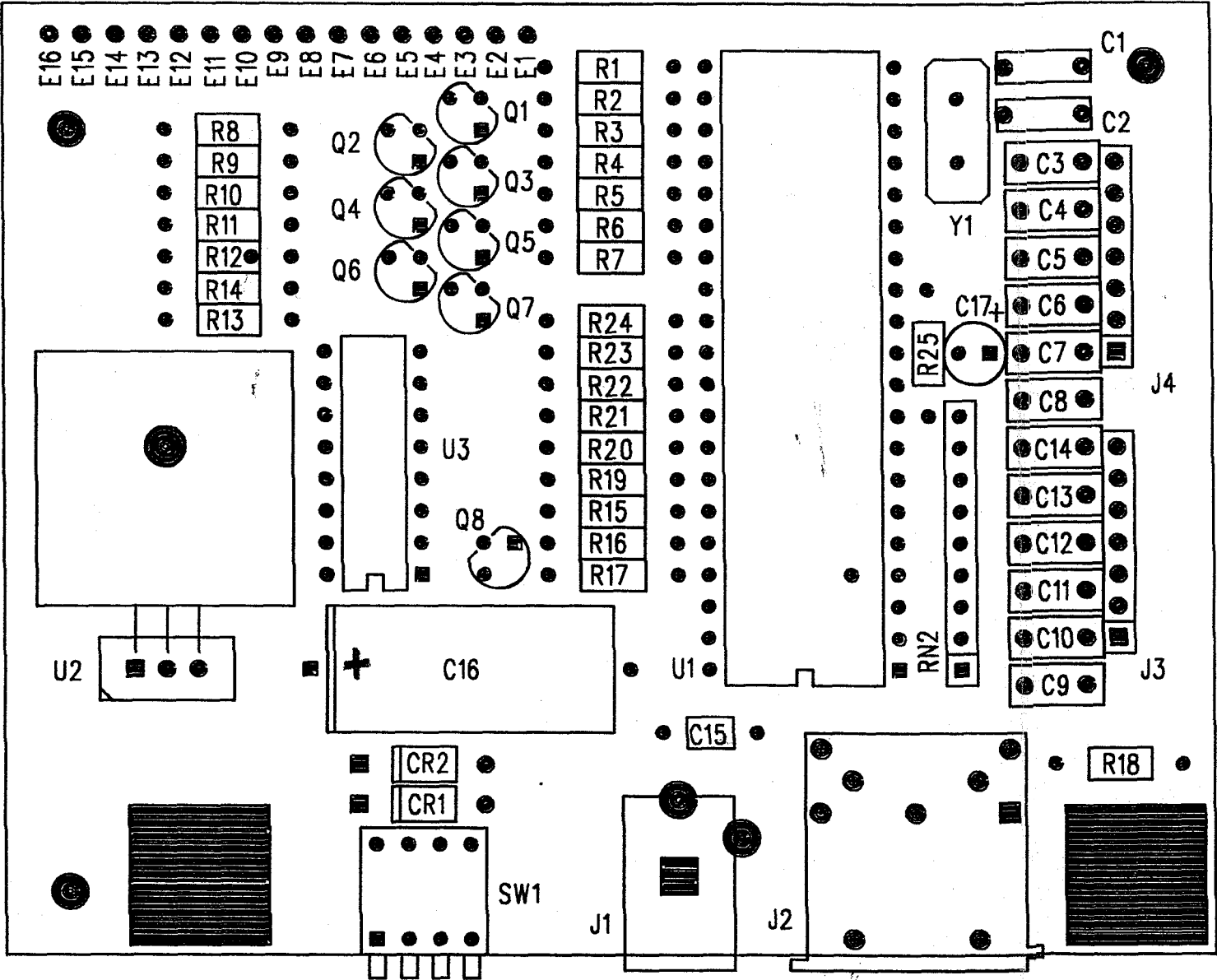
TOOLING AND MOUNTING HOLES


H1
H2
H3
H4
H5
H6

NOTES: UNLESS OTHERWISE SPECIFIED
2. ALL RESISTORS ARE 1/4W, 5%

COMPANY:		ADA INC.			
TITLE:		SCHEMATIC MPC BOARD			
DRAWN:	DATE:	CODE:	SIZE:	DRAWING NO.:	REV.:
ROW FARAR	11-3-83		D	848	A
CHECKED:	DATE:				
QUALITY CONTROL:	DATE:				
RELEASED:	DATE:				
SCALE: 1 : 1		SHEET: 1 OF 1			

REVISION HISTORY				
ZONE	REV.	DESCRIPTION	SIGNATURE	DATE
	A	RELEASE		11-3-93



THIS DRAWING DOCUMENT CONTAINS INFORMATION WHICH IS PROPRIETARY TO AND THE PROPERTY OF ADA, INC. AND MAY NOT BE USED, REPRODUCED OR DISCLOSED IN ANY MANNER WITHOUT THE EXPRESSED CONSENT OF ADA, INC.		 SIGNAL PROCESSORS, INC. 420 LESSER STREET, OAKLAND, CA 94601		
FILE NAME:	MPC_LD.JOB	TITLE LOADING DRAWING MPC BOARD		
CHECKED BY:		DRAWN:	RON FARAR	SIZE B
DATE:		ENGR:		DRAWING NO. 886
APPROVED BY:		DATE:	10-4-93	REV A
DATE:		SCALE:	2 : 1	SHEET 1 OF 1